

**IN THE SPECIFICATION:**

Please replace paragraph [0072] with the following amended paragraph:

--[0072] FIG. 14 is an exemplary waveform diagram showing a gate voltage generated from the gate driving circuit of FIG. 9, and a voltage change of a ferroelectric liquid crystal cell according to the gate voltage according to the present invention. In FIG. 14, the reference  $V_{lc}$  denotes the voltage of the ferroelectric liquid crystal cell  $Clc$ , and the reference  $V_{avg}$  denotes the average voltage charged to the ferroelectric liquid crystal cell  $Clc$  during the electric field alignment. The reference  $I_{on}$  denotes a leakage current generated by supplying the gate voltage, and the reference  $I_{off}$  denotes off current generated by supplying a voltage between gate voltages.--